Application/Con Applicant(s)/Patent Under Reexamination 09/963,533 ARAI, TAKAO Notice of References Cited Art Unit Examiner Page 1 of 1 2826

Johannes P Mondt

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,229,188 B1	05-2001	Aoki et al.	257/404
	В	US-6,465,842 B2	10-2002	Nishinohara et al.	257/330
	С	US-5,424,226	06-1995	Vo et al.	437/30
	D	US-			
	Ε	US-			
	F	US-			
	G	US-			
	Н	US-			
	ŀ	US-			
	7	US-			
	К	US-			
	L	US-			
	М	US-			

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q					
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	S. Wolf, "Silicon Processing for the VLSI Era", Volume 2 - Process Integration, Lattice Press, Sunset Beach, California, USA (1990), in particular pages 383-389.				
	V					
	w					
	х					

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.